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53 (IT 714) ACAR

2017

**ADVANCED COMPUTER  
ARCHITECTURE**

Paper : IT 714

Full Marks : 100

Time : Three hours

***The figures in the margin indicate  
full marks for the questions.***

***Answer any five questions out of eight.***

1. Explain Memory hierarchy. Clearly show speed-size characteristics and access size. 20
2. Can user program write to cache? 20
3. Can I-miss be dirty? If yes when? If no why not? 20
4. Explain write-through, and write-back caches. 20

Contd.

5. What is a 10 driver ? 20
6. Explain the difference between multi-programming, multiprocessing, multithreading. 20
7. What is TLB ? What does it do ? 20
8. Explain Interrupts. 20