SE DE TECHNOYO

END SEMESTER/RETEST EXAMINATION, 2020

Semester: 4th

Subject code: ET-405

Subject: ANALOGUE ELECTRONICS-II (new)

MARKS: 70 (part A-25 + Part B-45)

Duration: 3 hours

Questions on Part A are compulsory

Answer any five questions from Part B

PART-A MARK-25				
Question 1	Fill in the blanks	1x10=10		
1a	In double tuned amplifier for loose coupling, the circuit Q is			
1b	The tuned amplifiers are used to select and amplify a specific frequency.			
1c	Selectivity is in stagger tuned amplifier.			
1d	Positive feedback in amplifier increases the of the original signal.			
le .	Darlington pair provides very high current gain and input resistance.			
1f	In current-series feedback the output impedance is increased by the factor			
1g	In ideal op-amp, the B.W is			
1h	The 555 timer basically operates in mode.			
1i	The output shape of an op-amp integrator with a square wave input is wave.			
1j	Commercially available thin and thick film circuits are combinations of integrated and components.			
Question no 2	Write true or false:	1x10=10		
2a	The B.W is reduced in stagger tuned amplifier.			
2b	Negative feedback in amplifier reduces B.W.			
2c	Emitter follower provides better frequency response than a transformer for impedance matching.			
2d	Input offset voltage is the voltage, applied between input terminals to balance the amplifier.	LIBRARI		
2e	If CMRR is very low, the efficiency of an op-amp is very high.	1 %		

5=5
8 (50)
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Question no.	Questions	marks
Question no. 4		
Q4a	Differentiate between series and shunt feedback amplifier.	3
Q4b	Describe how gain is stabilized and distortion is reduced.	6
Question no5	/Stalk	AL LIGRARY

Q5a	Describe the operation of differential amplifier in double ended input and output with neat diagram.	6
Q5b	Write the significance of CMRR.	3
Question no. 6		
Q6a	Discus bias current, offset voltage, and open loop gain in case of opamp.	6
Q6b	Describe with neat diagram op-amp as integrator.	3
Question no. 7		-399
Q7a	What is barkhausen criterion for oscillation?	3
Q7b	Draw the circuit diagram of colpitt oscillator and briefly explain how the oscillation is maintained.	6
Question No8		
Q8a	Give the function of clamper circuit.	2
Q8b	Explain the working of positive and negative clamper with proper diagram.	7
Question No 9		
Q9a	State the basic difference between Astable and Bistable M.V	2
Q9b	Describe monostable M.V using IC 555 with proper diagram.	- 7
Question no10		
Q 10a	What are the applications of sweep circuits?	2
Q10b	Describe the operation of Miller sweep circuit.	7
Question no 11		N.
Q 11a	Write the advantages of ICs over discrete elements.	3
Q 11b	Discuss crystal growth of substrate and epitaxial growth of ICs in respect of production process for the monolithic ICs.	6
