## CAI-405/EC&M/4th Sem/2017/N

## **ELECTRONIC COMPONENTS AND MATERIALS**

Full Marks - 70

Time - Three hours

The figures in the margin indicate full marks for the questions.

PART-A

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Marks - 25

Answer all the questions.

1.	Determine	the	correct	options	for	the	following
	questions:						1×10=10

(i)	The number of valence electrons in pentavaler					
	impurity is					

(a) 5

(b) 3

(c) 4

(d) 1

(ii)	If the diameter of a wire is doubled, its current carrying capacity becomes					
	(a)	one-fourth	(b)	twice		
	(c)	half	(d)	four times.		
(iii)	Materials which lack permanent magnetic dipoles are known as					
	(a)	paramagnetic	(b)	diamagnetic		
	(c)	ferromagnetic	(d)	ferrimagnetic.		
(iv)	and are	A parallel plate capacitor has its length, width and separation doubled. Its fringing effects are neglected, to keep the capacitance same, the dielectric constant must be				
	(a)	halved	(b)	keep the same		
	(c)	doubled	(d)	made four times.		
(v)	At room temperature, the current in intrinsic semiconductor is due to					
	(a)	holes		di linse - D		
	(b)	electrons				
	(c)	ions				
	(d)	both holes and	d elec	ctrons.		
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(vi)	Which of the following is a donor impurity?					
	(a)	gold	(b)	Indium		
	(c)	boron	(d)	Phosphorus.		
(vii)		ed on the fabrical	ation 1	technique an IC can		
	(a)	2 types	(b)	3 types		
	(c)	4 types	(d)	5 types.		
(viii) Which of the following is an alloy?						
	(a)	Copper	(b)	Nichrome		
	(c)	Nickel	(d)	Lead.		
(ix)	The	units of $\mu_0$ and	μ, are	er salmine		
	(a) H/m for both					
	(b) H/m for $\mu_r$ and no units for $\mu_0$					
	(c) H/m for $\mu_0$ and no units for $\mu_r$					
	(d)	Wb/m for $\mu_0$ a	nd no	units for $\mu_r$ .		
(x)	) If the temperature of an extrinsic semiconductor is increased so that the intrinsic carrier concentration is doubled, then					
	(a) majority carrier density is doubled					

- (b) minority carrier density is doubled
- (c) majority carrier density becomes four times the original value
- (d) both majority and minority carrier density are doubled.
- 2. State whether the following statements are true or false:  $1 \times 10=10$ 
  - (i) There is no hysteresis phenomenon in any dielectric material.
  - (ii) Diamond is a paramagnetic material.
  - (iii) In crystalline solids, atoms are stacked in a regular manner.
  - (iv) In intrinsic semiconductors, the number of free electrons is equal to the number of mobile holes.
  - (v) Rochelle salt is a piezo electric material.
  - (vi) At a very low temperature, a semiconductor becomes a conductor.
  - (vii) Thermistor is used in temperature measurement.
  - (viii) Gallium arsenide is a semiconductor.

(ix) Ganged capacitor is a variable capacitor. (x) The resistivity decreases on alloying two materials. Fill in the blanks: 1×5=5 (i) Constantan is an (ii) Transformer works on the principle of Inductance. (iii) Paper capacitor uses as dielectric material. (iv) The ICs having number of transistors between 100 - 1000 is known as (v) Nickel is a material. PART - B Marks - 45 Answer all the questions. State the intrinsic and extrinsic factors affecting conductivity of a material. 10 Explain the manufacturing process of carbon-film resistors. 10

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5.

- Define the terms: dielectric constant and dielectric strength.
- 7. What are the different types of core losses in transformer? Explain.
- 8. Write short notes on any three:  $5\times 3=15$ 
  - (a) Ganged capacitor
  - (b) Classification of IC
  - (c) Hard and soft magnet
  - (d) Ceramic materials.